



VDAT 2009 - CALL FOR PARTICIPATION

Thirteenth IEEE VLSI Design And Test Symposium

July 8-10, 2009 – Bangalore

Learning Centre, Wipro Campus, Electronics City, Hosur Road, Bangalore



Call for participation:

The 13th IEEE VLSI Design And Test Symposium (VDAT2009) is scheduled during July 8-10, 2009 at Bangalore. Authors are invited to submit unpublished work in a maximum of 10 A4 size pages including figures, appendices and references. The submissions will undergo double-blind reviews: **authors must not indicate their names or affiliations anywhere in the text**, and if possible, in the references. The submissions should represent original contributions and should not have been submitted to other forums, printed journals or conferences. Reusing material from published work, even if the authors' own is tantamount to *plagiarism* and *is strongly discouraged*.

Topics:

In the past, VDAT has focused on the Design and Test of VLSI chips and systems. This year, we wish to steer the conference significantly towards new and emerging areas. Some of the key areas where we are soliciting papers are:

- **Area A** - VLSI and Embedded Systems for Medical Applications and Healthcare
- **Area B** - VLSI and Embedded Systems for Energy Conversion, including Solar Inversion
- **Area C** - VLSI and Embedded Systems for Communications and Computing
- **Area D** - VLSI and Embedded Systems for Other Applications, including Rural
- **Area E** – System Integration and Test
- **Area F** – VLSI Chip Design and Test

Subject area categories

Alternate Energy	Computer applications in Medicine	Medical imaging	Solar/PV Inverters
Battery Management	Digital X-Ray	Medical instrumentation and Measurements	System integration and Test
Biomechanics and Rehabilitation Engineering	Energy Conversion	Power Management	Telehealth
Biomedical Applications	Energy Harvesting	Precision Agriculture	Telemedicine
Biomedical Electronics	Environment Preservation	Rural Applications	Ultrasound
Biomedical Signal Processing	Medical Electronics Solutions	Rural Communication	VLSI Chip Design and Test
Communications and Computing	Medical image Compression	Smart Phones	

Please indicate the area clearly when you make the submission. The area must also be indicated in the first page of the manuscript, below the title. Please do not submit survey papers. **This year, we will publish the proceedings after the conference. Only the papers that were presented at the symposium will be considered for inclusion in the proceedings.** As a result, there will be no requirement of submission of camera-ready papers before the conference. It is assumed that at least one author will register and attend the conference to present the paper. Hardcopy proceedings will be mailed to attendees within 6 months of the event.

Authors may also submit proposals for half-day or full-day tutorials in the areas mentioned above. In this case, you must indicate this by placing the text "Tutorial Submission" below the title and providing the details of the speaker(s). Each tutorial submission must consist of an extended abstract. We encourage tutorials in the emerging areas.

Please visit Microsoft's Conference Management Service <https://cmt.research.microsoft.com/VDAT2009/Default.aspx> for paper submission. Help on using this service is available through <https://cmt.research.microsoft.com/Cmt2Help/>

Important dates:

- Last Date for Paper and Tutorials Submission** – April 10, 2009 ^{Completed}
- Last Date for Intimation of Acceptance** – June 1, 2009
- Last Date for Authors to confirm their participation** – June 7, 2009
- Full-day Tutorials** – July 8, 2009
- Conference Dates** – July 9 – 10, 2009

Submission guidelines

Authors of accepted papers will be required to submit full papers, and Presentation Foils of their talks in PowerPoint or PDF format. Final submissions (full papers or presentations) will be made available as Proceedings. **Please visit guidelines to authors on homepage** and <http://vlsi-india.org/docs/i2a-doc.html> for page formatting guidelines.

Proposals for full-day (6 hours' duration) or half-day (3 hours' duration) tutorials are also invited. Tutorial proposals should have (i) Tutorial title and short summary (in about 200 words), (ii) Name, affiliation and contact details of the co-coordinating presenter and all other co-presenters, (iii) Target audience and prerequisites along with beneficial impact of the tutorial on the intended participants (in about 75 words), (iv) detailed tutorial program, delineated sub-topic-wise description of the structure with time duration of each (in about 2000 words), (v) short biographical sketch of each presenter (in about 200 words for each presenter).

Proposals for embedded tutorials (1 hour duration) and panel discussions are also invited. The proposals must include the full papers.

Submitting authors must become members of the vdat@yahogroups.com mailing list, where updates on the symposium will be sent. Send any queries related to registration and local arrangements only to vdat@vlsi-india.org

VDAT History:

Event Title	Venue	Date	Participants
1 st VDAT	Chennai	January 7, 1998	30
2 nd VDAT	New Delhi	August 6-7, 1998	70
3 rd VDAT	New Delhi	August 20-21, 1999	120
4 th VDAT	New Delhi	August 25-26, 2000	150
5 th VDAT	Bangalore	August 16-18, 2001	220
6 th VDAT	Bangalore	August 29-31, 2002	300
7 th VDAT	Bangalore	August 28-30, 2003	300
8 th VDAT	Mysore	August 26-28, 2004	250
9 th VDAT	Bangalore	August 10-13, 2005	350
10 th VDAT	Goa	August 9-12, 2006	250
11 th VDAT	Kolkata	August 8-11, 2007	250
12 th VDAT	Bangalore	July 23-26, 2008	200

Conference committee

General Chair: C.P. Ravikumar, Texas Instruments India
Finance Chair: S.R.Gopal Naidu, Texas Instruments India
The website will be updated with more details of the Organizing Committee.

Corporate Support for VDAT

Companies that wish to provide corporate support for VDAT may contact the Finance Chair (vsiaccounts@vlsi-india.org) or General Chair (ravikumar@vlsi-india.org)

There will be provision for product exhibits.

Fellowships

Fellowships are available for Indian postgraduate students and faculty working in the areas related to VLSI. A fellowship should not be viewed as an honor or recognition, but as an enabler to attend VDAT for students/faculty who do not get adequate support. Please do not apply if your organization can support you or you can support yourself. Fellowship entitles you to a lower registration fee. No other support will be available. We may be able to provide hostel rooms for fellows on a self-payment basis. The students who apply for fellowship must be students at the time of the symposium. Application details would be announced on VDAT website.