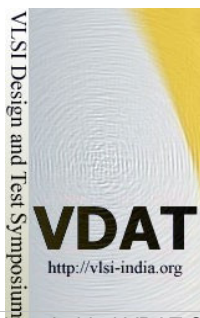


Call for Participation 11th IEEE VLSI Design and Test Symposium

<http://vlsi-india.org/>
August 8 – 11, 2007

Saha Institute of Nuclear Physics Convention Centre, Salt Lake
Kolkata, India



The idea behind VDAT Symposium is to promote R&D on all aspects of VLSI in India, and to act as a forum for free discussion on hot and emerging topics on VLSI and the related for VLSI professionals from India and abroad. This year there will also be a special pre-symposium event of a one-day **Workshop on Nanotechnology**.

Tracks in VDAT 2007

- **High-Level Design** will focus on System Level Design and Synthesis, Microarchitecture, Embedded Systems, Co-Design, Core-Based Designs, High-Level Synthesis, Logic Synthesis, Memory Synthesis, and FPGA Synthesis.
- **Physical Design and VLSI Technology** will focus on Physical Design and Technology and Process-Related Aspects of Integrated Circuits. It will also discuss issues on Optoelectronic Devices and Circuits, MEMS, Deep Submicron and Nanometer Devices and Circuits.
- **Testing and Verification** will focus on issues related to Testing, Testability and Verification of Digital Designs, Memories, Analog and Mixed-Signal Designs.

Call for Technical papers, Proposals for Tutorials and Embedded Tutorials on topics related to VLSI design, VLSI testing, Formal Verification, and CAD tools

Authors are invited to submit unpublished work in a maximum of 10 A4 size pages including figures, appendices and references. The submissions will undergo double-blind reviews: **authors must not indicate their names or affiliations anywhere in the text**, and if possible, in the references. The submissions should represent original contributions and should not have been submitted to other forums, printed journals or conferences. Reusing material from published work, even if the authors' own is tantamount to *plagiarism* and *is strongly discouraged*. Authors of accepted papers will be required to submit full papers, and Presentation Foils of their talks in PowerPoint or PDF format. Final submissions (full papers or presentations) will be made available as Proceedings. **Please visit guidelines to authors on homepage** and <http://vlsi-india.org/docs/i2a-doc.html> for page formatting guidelines.

Proposals for full-day (6 hours' duration) or half-day (3 hours' duration) tutorials are also invited. Tutorial proposals should have (i) Tutorial title and short summary (in about 200 words), (ii) Name, affiliation and contact details of the co-ordinating presenter and all other co-presenters, (iii) Target audience and prerequisites along with beneficial impact of the tutorial on the intended participants (in about 75 words), (iv) detailed tutorial program, delineated sub-topic-wise description of the structure with time duration of each (in about 2000 words), (v) short biographical sketch of each presenter (in about 200 words for each presenter). Tutorials targeting Postgraduate and Ph. D. students, junior Faculty Members and greenhorn professionals are likely to get preference.

Proposals for embedded tutorials (1 hour duration) and panel discussions are also invited. The proposals must include the full papers.

NOTE: Both PDF and DOC formats are allowed for initial submissions with a 5MB size limit.

Technical submission

On-line paper submission/review for VDAT Symposium is managed by Microsoft's CMT (Conference Management Toolkit). Please use the following link for making paper submissions to VDAT 2007: <https://msrcmt.research.microsoft.com/VDAT2007/> Click on "User Login." You may need to create your login ID and profile by clicking "New User Registration". The instructions are self-explanatory. The above link is also accessible from <http://vlsi-india.org/events/vdat2007/> The websites will open from February 2007.

For questions relating to technical program, paper submissions etc. please contact

Prof. S. Sur-Kolay, ssk@isical.ac.in
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Prof. D.Mukhopadhyay, dmukho@yahoo.com

For questions relating to sponsorship etc. please contact

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ravikumar@vlsi-india.org

Submitting authors must become members of the mailing list of the group-mail vdat@yahoogroups.com, where updates on the conference will be available. Please send any queries on registration and local arrangements only to vdat07@gmail.com

IMPORTANT DATES

Last Date for Paper submission: April 4, 2007 <small>Extended</small>	Last day to receive Fellowship Application: June 1, 2007
Last Date for Tutorial submission: April 4, 2007 <small>Extended</small>	Submission of final camera-ready manuscript: June 15, 2007
Notification of acceptance: May 15, 2007	Symposium Dates: August 8-11, 2007

Accommodation and Local Arrangements

The Conference Venue is about 20 km from the Airport, about 30 km from Howrah Railway Station and about 25 km from the Sealdaha Railway Station. Budget hotels (Rs 500 per Diem upwards) are available in the central part of the city, about 20 km off the venue. Arrangements will be made for some low-tariff (<Rs 400 per Diem) accommodation near the venue. Details about some selected hotels and guesthouses will be made available at the VDAT 2007 website in March 2007. Please note that the VDAT 2007 organizing committee will not make arrangements for accommodation of the participants.

Fellowship

Fellowships are available for Indian postgraduate students and faculty working in the areas related to VLSI. A fellowship should not be viewed as an honor or recognition, but as an enabler to attend VDAT for students/faculty who do not get adequate support. Please do not apply if your organization can support you, or if you have availed fellowship in the previous year, or you can support yourself. Fellowship entitles you to a lower registration fee. *No other support will be available.* We plan to get hostel rooms for fellows at nominal rates. The students/faculty who apply for fellowship must be students/faculty at the time of the symposium.

Application forms must be forwarded to Fellowships Chair by email vdat07-fellow@gmail.com no later than June 1, 2007.

Symposium Committee

General Co-chairs

CP Ravikumar, Texas Instruments India

BB Bhattacharya, Indian Statistical Institute

Technical Program Chairs

S Sur-Kolay, Indian Statistical Institute
Parthasarathi Dasgupta, IIM Calutta
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Technical Program Committee

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DK Sharma, IIT, Bombay
B Sikdar, Bengal Engg & Science University
S Srinivasan, IIT, Madras
L Vemury, Silicon Image, USA
V Visvanathan, Texas Instruments, India

History of VDAT:

Event	Venue	Date	Participants
1 st VDAT	Chennai	January 7, 1998	30
2 nd VDAT	New Delhi	August 6-7, 1998	70
3 rd VDAT	New Delhi	August 20-21, 1999	120
4 th VDAT	New Delhi	August 25-26, 2000	150
5 th VDAT	Bangalore	August 16-18, 2001	220
6 th VDAT	Bangalore	August 29-31, 2002	300
7 th VDAT	Bangalore	August 28-30, 2003	300
8 th VDAT	Mysore	August 26-28, 2004	250
9 th VDAT	Bangalore	August 10-13, 2005	320
10 th VDAT	Goa	August 9-12, 2006	220

Program

Day 1-2: Technical paper presentations, Panel Discussions and Keynote addresses from the renowned in VLSI field.

Day 3: VLSI Education Day

Day 4: Tutorials (Refer Tracks in VDAT2007)

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